

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	185	ELAN same switch	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/05/13 16:29
L2	2	(rout\$3 and ELAN and VLAN).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/05/13 16:32
L3	1	(rout\$3 and ((transit or bridge) adj ELAN) and VLAN).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/05/13 16:33
L4	1	((transit or bridge) adj ELAN) and VLAN).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/05/13 16:35
L5	1	((bridge near2 (ELAN)) and VLAN).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/05/13 16:35
L6	1	((transit near2 (ELAN)) and VLAN).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/05/13 16:34
L7	4	((ELAN) and LEC and VLAN).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/05/13 16:35
L8	1	((access adj function) and ELAN and LEC).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/05/13 16:36
L9	10	((access) and ELAN and LEC).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/05/13 16:54
L10	6	((access) and rout\$3 and ELAN and LEC).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/05/13 16:54

Inventor Name Search Result

Your Search was:

Last Name = BAVANT

First Name = MARC

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>09147510</u>	6330228	150	01/12/1999	METHOD AND DEVICE FOR MEASURING THE PROPORTION OF AN ATM CELL STREAM SATISFYING A DETERMINED CRITERION	BAVANT, MARC
<u>09147511</u>	6498780	150	01/12/1999	METHOD FOR DETECTING UPSTREAM CONGESTION IN AN ATM COMMUTATOR WITH A SWITCHING SYSTEM ARCHITECTURE AND INPUT MEMORISATION	BAVANT, MARC
<u>09147512</u>	6144978	150	01/12/1999	METHOD AND DEVICE FOR THE APPROXIMATIVE COMPUTATION OF THE EXPONENTIAL MEAN OF A SEQUENCE OF CODED NUMBERS IN FLOATING POINT FORMAT	BAVANT, MARC
<u>09147667</u>	6556570	150	02/10/1999	METHOD FOR TRANSLATING AN ATM SWITCH CELL HEADER	BAVANT, MARC
<u>09175478</u>	6606302	150	10/20/1998	METHOD FOR THE CONTROL OF FLOWS WITHIN AN ATM SWITCH WITH DISTRIBUTED ARCHITECTURE	BAVANT, MARC
<u>09284248</u>	6529473	150	04/15/1999	DEVICE FOR ATM CELL SWITCHING, WITH FAILURE TOLERANCE	BAVANT, MARC
<u>09530948</u>	Not Issued	161	05/15/2000	METHOD FOR RELAYING IP APPLICATION FRAMES IN AN ATM SWITCH WITH DISTRIBUTED NETWORK ARCHITECTURE	BAVANT, MARC
<u>09895324</u>	Not Issued	71	07/02/2001	Method for the routing of IP frames between the users of a variable graph network	BAVANT, MARC
<u>10083128</u>	Not Issued	30	02/27/2002	Method and device to transfer data in a communication network comprising a low-bit rate artery	BAVANT, MARC
<u>11119494</u>	Not	40	04/29/2005	Method for translating an ATM	BAVANT, MARC

	Issued			switch cell header	
<u>11203654</u>	Not Issued	19	08/12/2005	Method for the control of flows within an ATM switch with distributed architecture	BAVANT, MARC
<u>11365812</u>	Not Issued	19	03/02/2006	Method for the routing of IP frames between the users of a variable graph network	BAVANT, MARC

Inventor Search Completed: No Records to Display.

Search Another: Inventor

Last Name	First Name	
<input type="text" value="BAVANT"/>	<input type="text" value="MARC"/>	<input type="button" value="Search"/>

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Inventor Name Search Result

Your Search was:

Last Name = DELATTRE

First Name = MICHEL

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>08793899</u>	5866380	150	06/12/1997	METHODS FOR BIOCONVERSION OF FERULIC ACID TO VANILLIC ACID OR VANILLIN AND FOR THE BIOCONVERSION OF VANILLIC ACID TO VANILLIN USING FILAMENTOUS FUNGI	DELATTRE, MICHEL
<u>09147511</u>	6498780	150	01/12/1999	METHOD FOR DETECTING UPSTREAM CONGESTION IN AN ATM COMMUTATOR WITH A SWITCHING SYSTEM ARCHITECTURE AND INPUT MEMORISATION	DELATTRE, MICHEL
<u>09147667</u>	6556570	150	02/10/1999	METHOD FOR TRANSLATING AN ATM SWITCH CELL HEADER	DELATTRE, MICHEL
<u>09175478</u>	6606302	150	10/20/1998	METHOD FOR THE CONTROL OF FLOWS WITHIN AN ATM SWITCH WITH DISTRIBUTED ARCHITECTURE	DELATTRE, MICHEL
<u>09216031</u>	6162637	250	12/18/1998	ASPERGILLUS NIGER WHICH PRODUCES VANILLIC ACID FROM FERULIC ACID	DELATTRE, MICHEL
<u>09284248</u>	6529473	150	04/15/1999	DEVICE FOR ATM CELL SWITCHING, WITH FAILURE TOLERANCE	DELATTRE, MICHEL
<u>09530948</u>	Not Issued	161	05/15/2000	METHOD FOR RELAYING IP APPLICATION FRAMES IN AN ATM SWITCH WITH DISTRIBUTED NETWORK ARCHITECTURE	DELATTRE, MICHEL
<u>09895324</u>	Not Issued	71	07/02/2001	Method for the routing of IP frames between the users of a variable graph network	DELATTRE, MICHEL
<u>11108911</u>	Not Issued	30	04/19/2005	Method of routing in an AD HOC network	DELATTRE, MICHEL
<u>11203654</u>	Not Issued	19	08/12/2005	Method for the control of flows within an ATM switch with distributed architecture	DELATTRE, MICHEL

<u>11365812</u>	Not Issued	19	03/02/2006	Method for the routing of IP frames between the users of a variable graph network	DELATTRE, MICHEL
<u>60773896</u>	Not Issued	19	02/15/2006	Nonwoven articles with multi- lobal fabrics	DELATTRE, MICHEL

Inventor Search Completed: No Records to Display.

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